

FIG. 2A

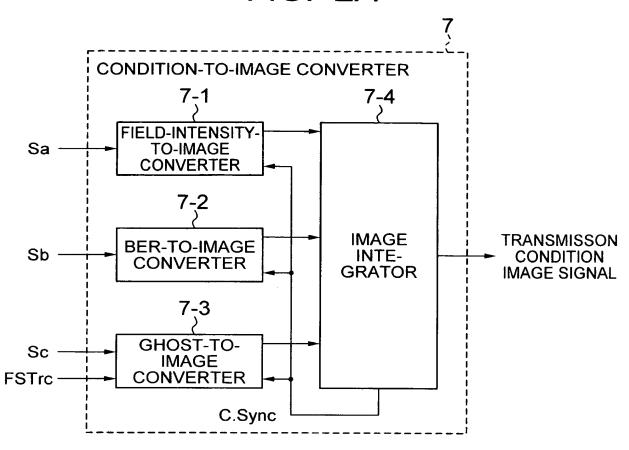


FIG. 2B

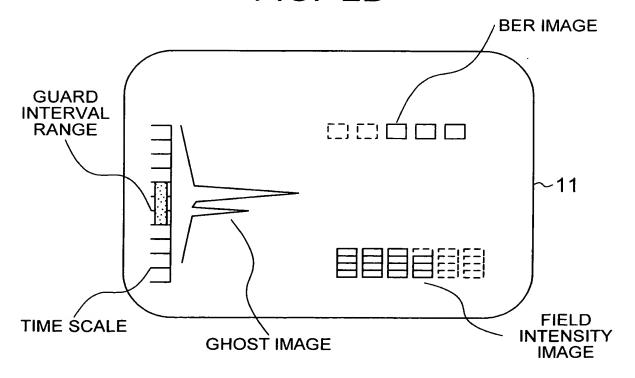


FIG. 3A

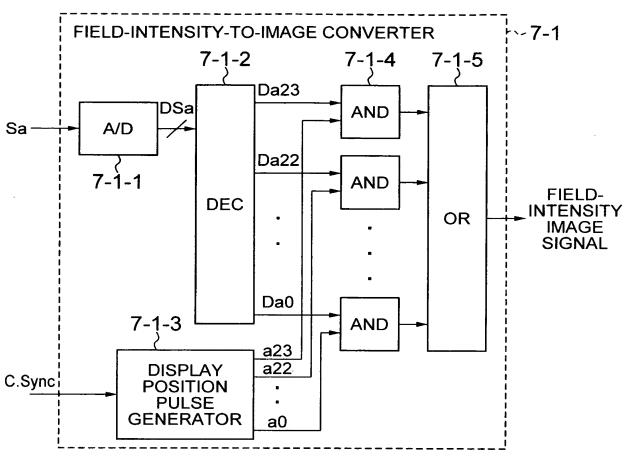


FIG. 3B

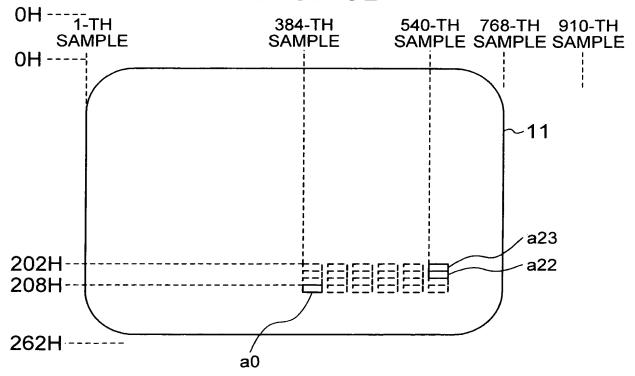


FIG. 4A

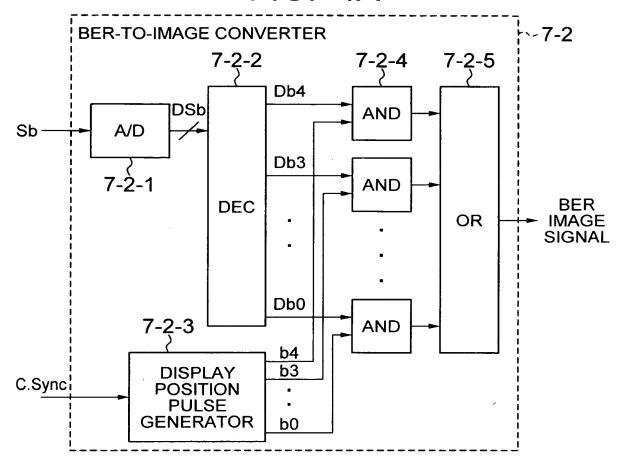


FIG. 4B

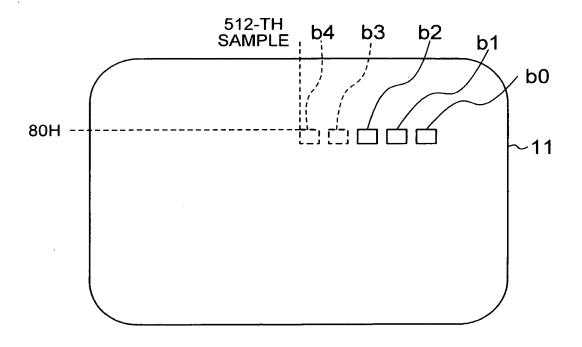


FIG. 5

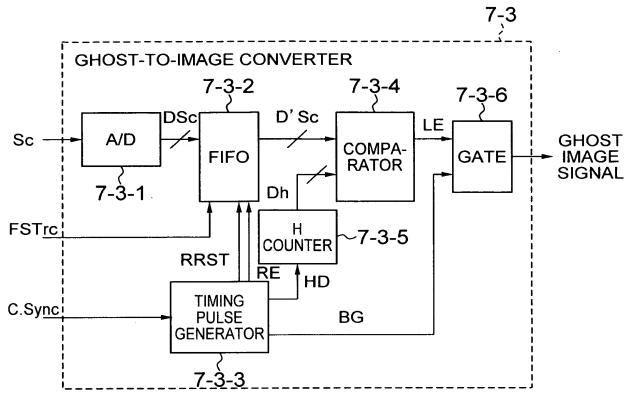


FIG. 6

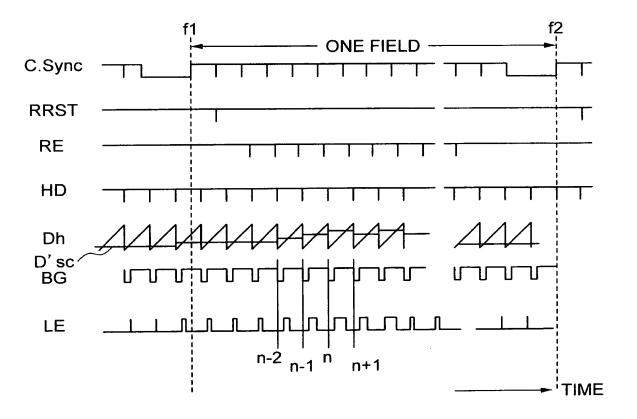


FIG. 7

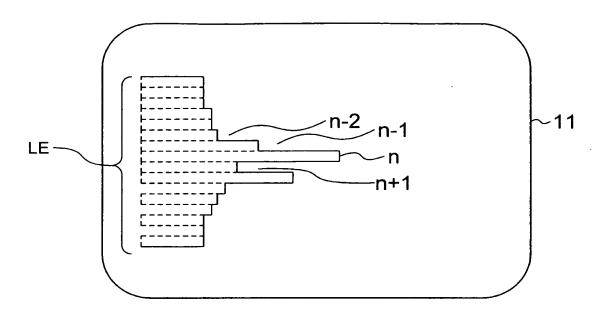


FIG. 8

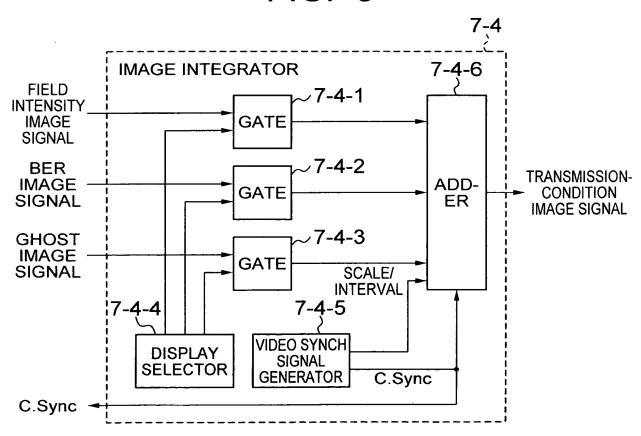


FIG. 9A

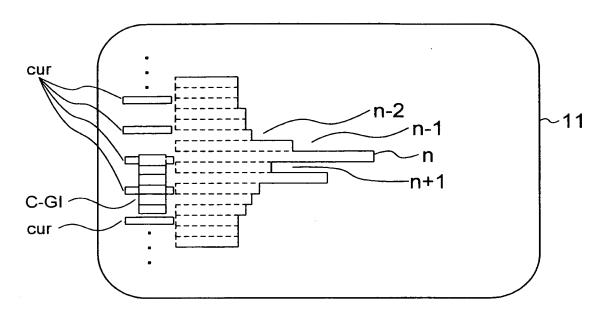
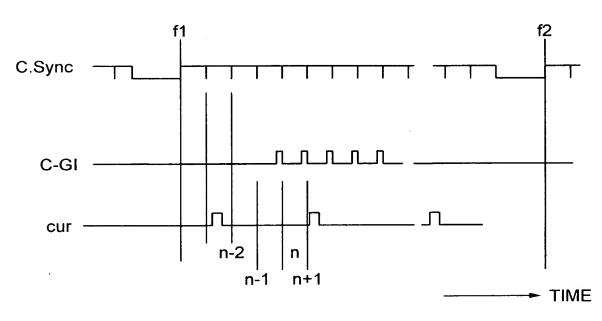


FIG. 9B



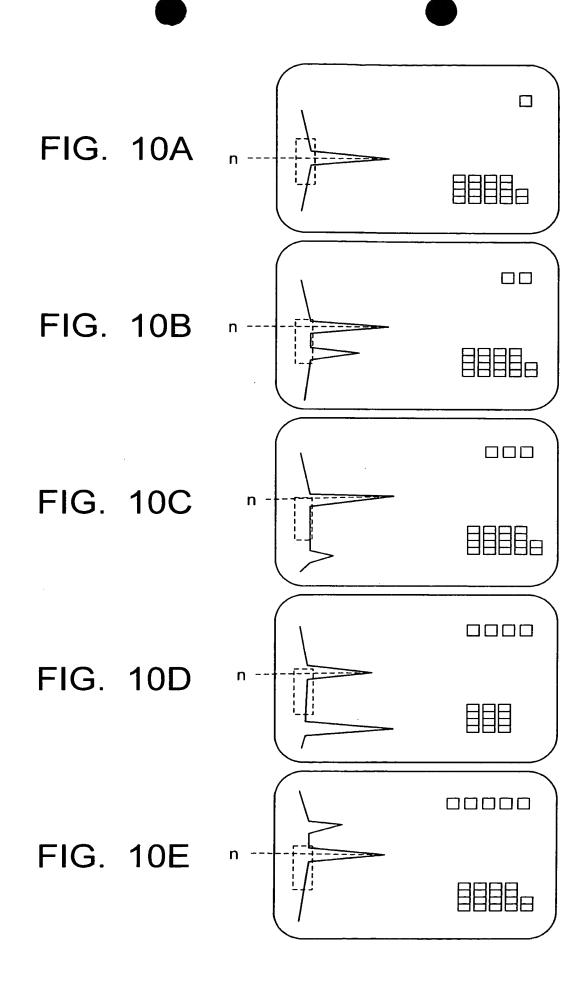
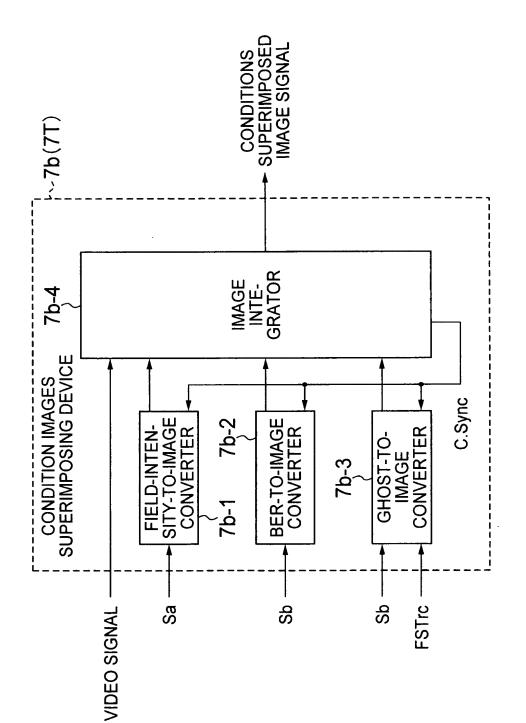


FIG. 11



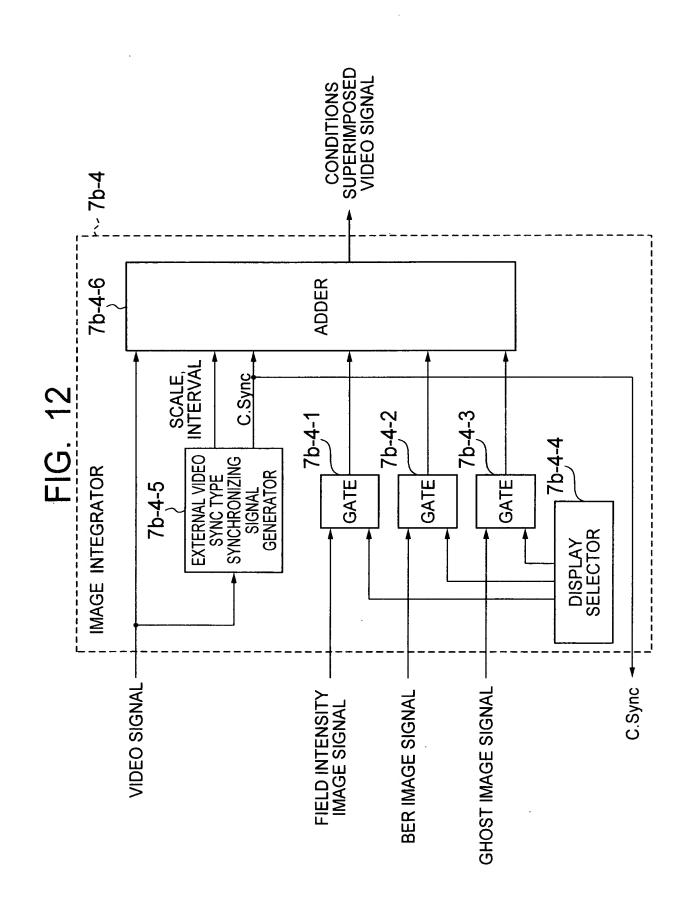


FIG. 13

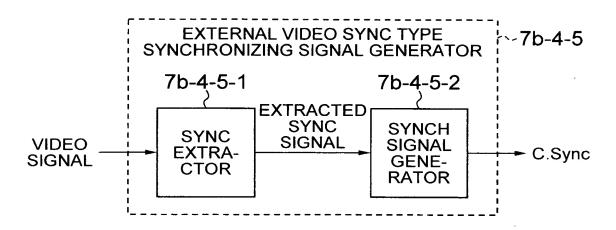


FIG. 14

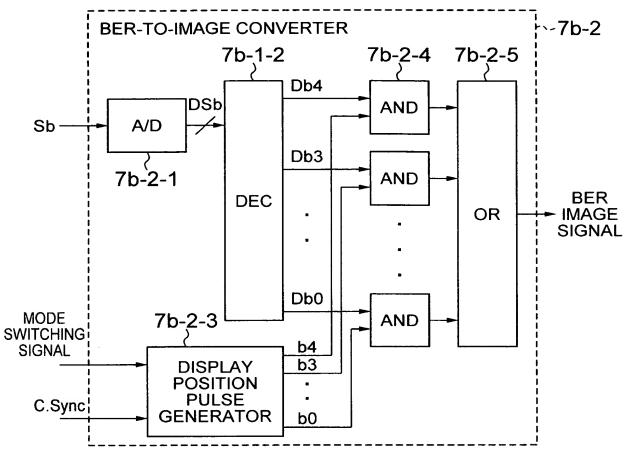
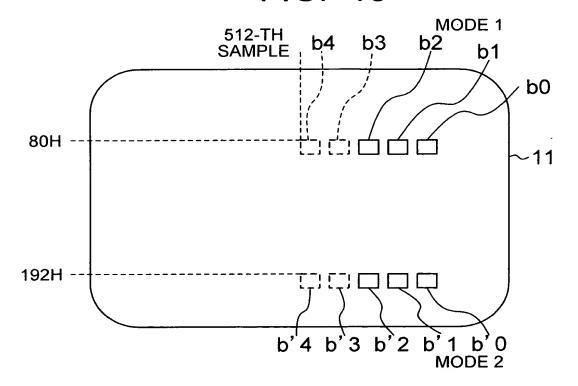


FIG. 15



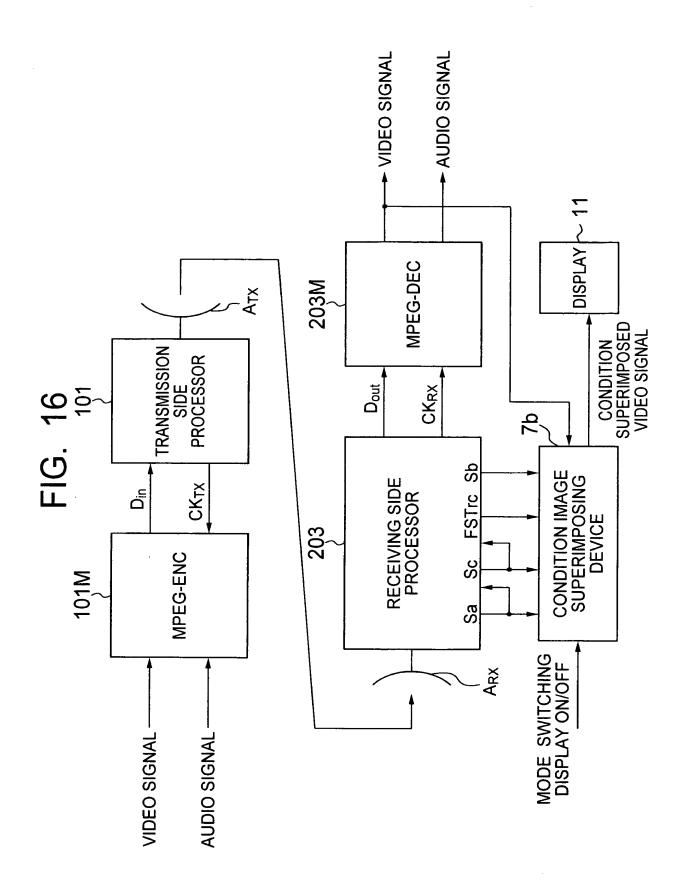


FIG. 17

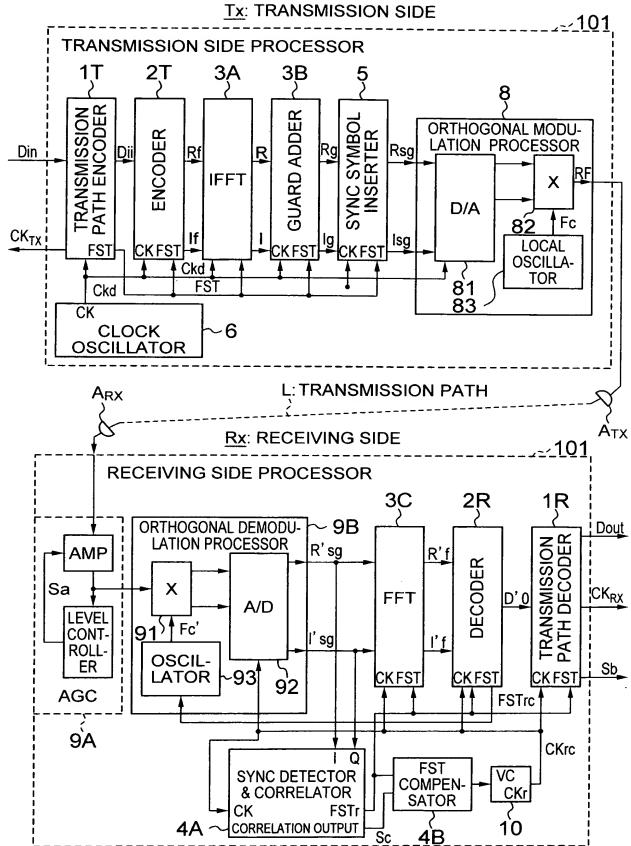


FIG. 18

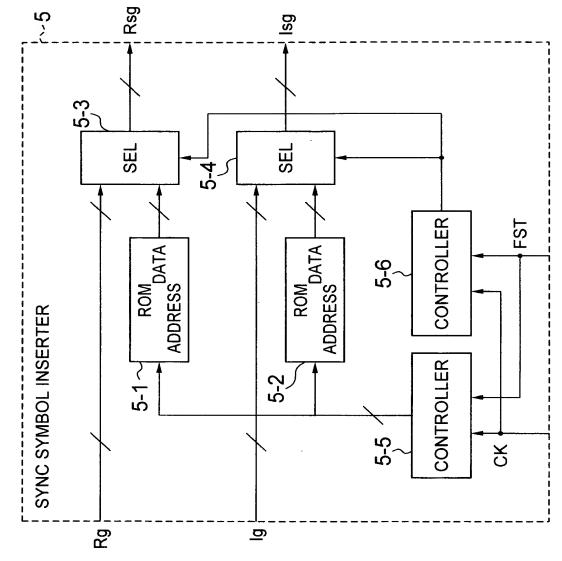
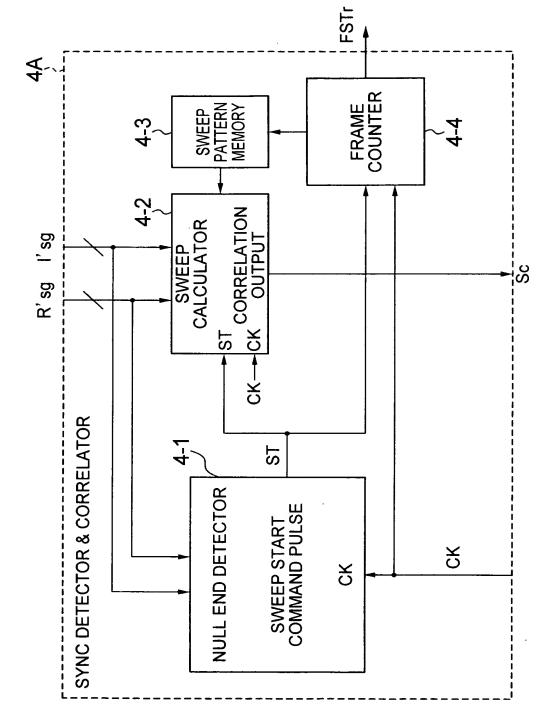


FIG. 19



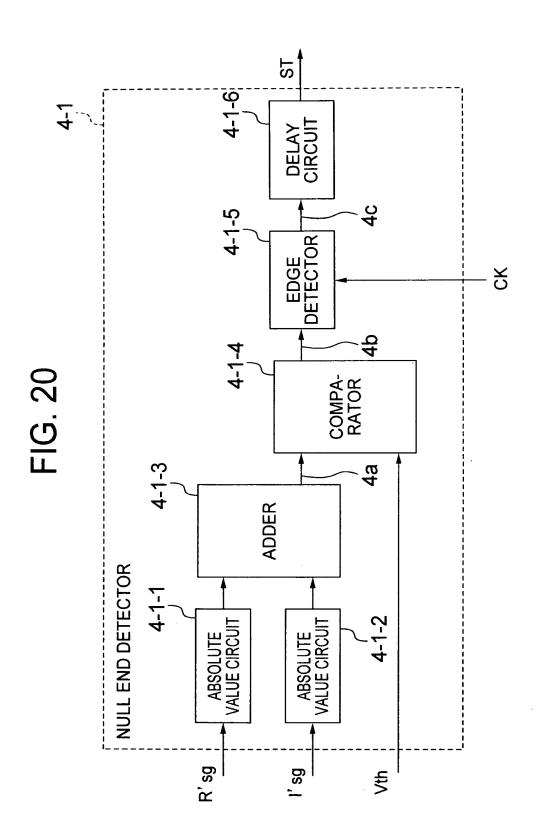


FIG. 21

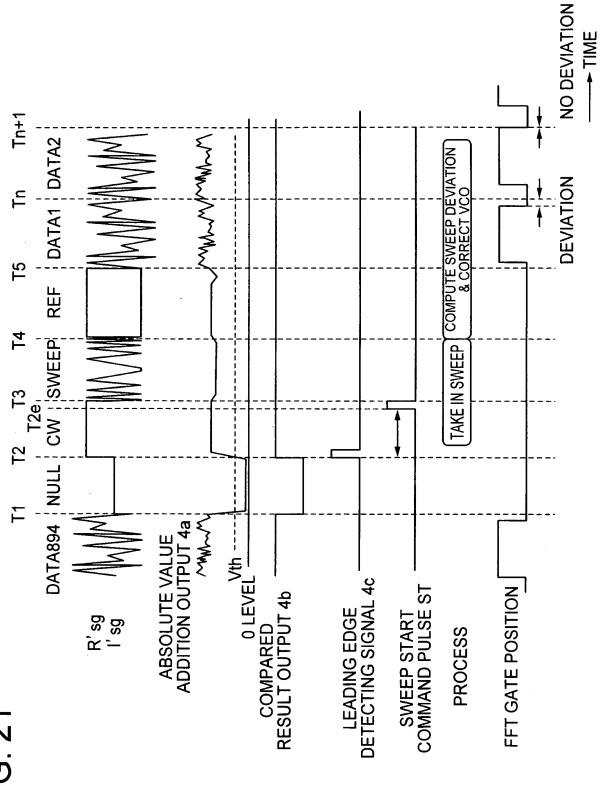


FIG. 22A

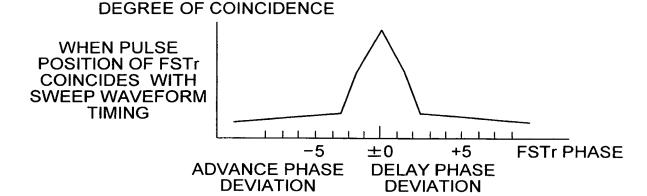


FIG. 22B

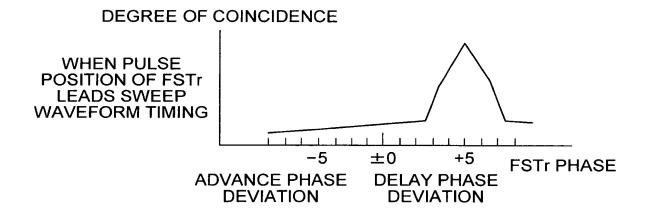
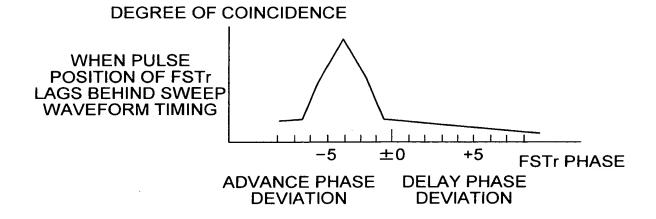


FIG. 22C



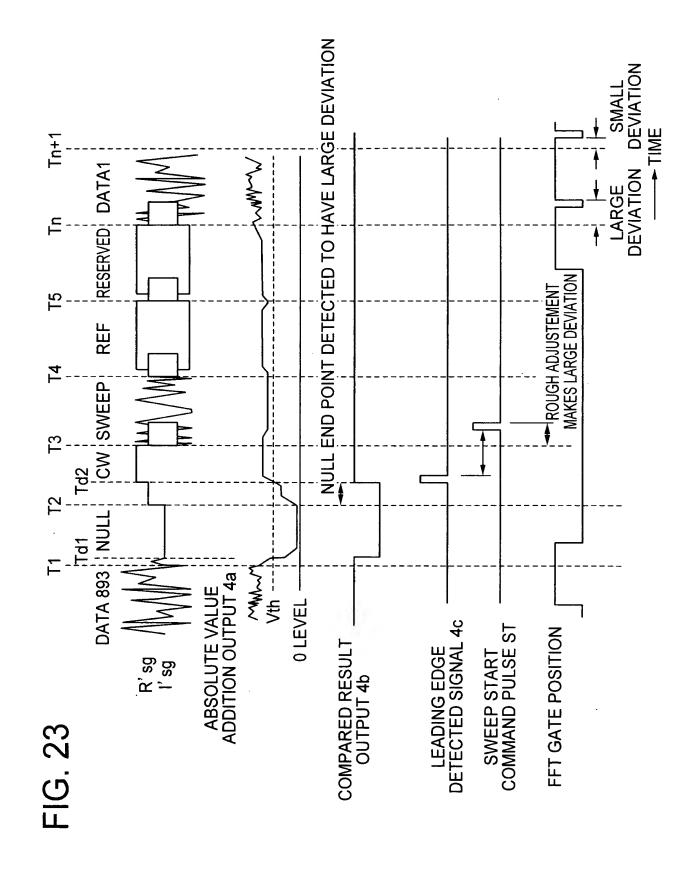


FIG. 24

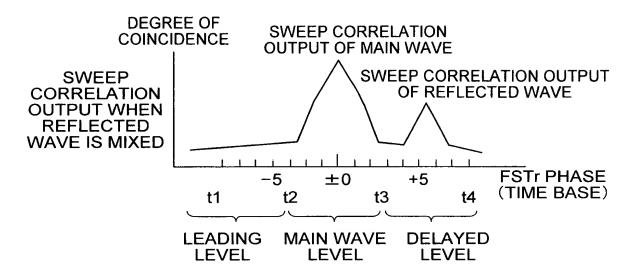


FIG. 25

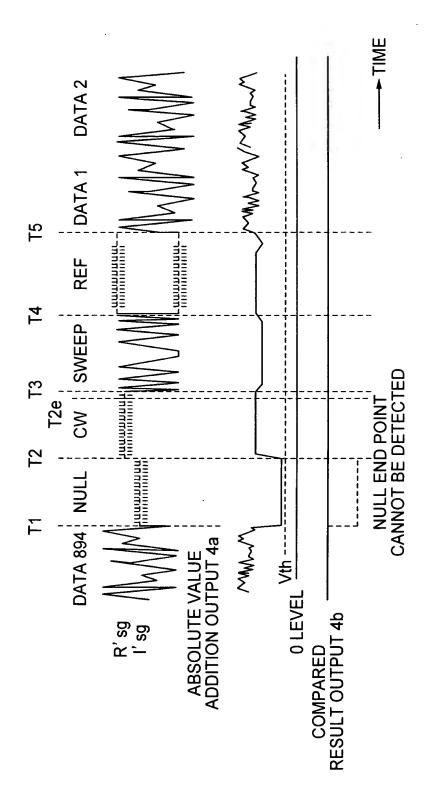


FIG. 26

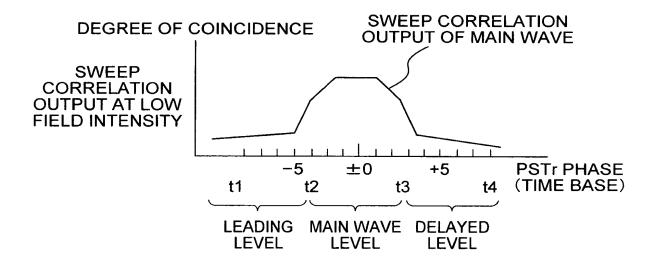


FIG. 27

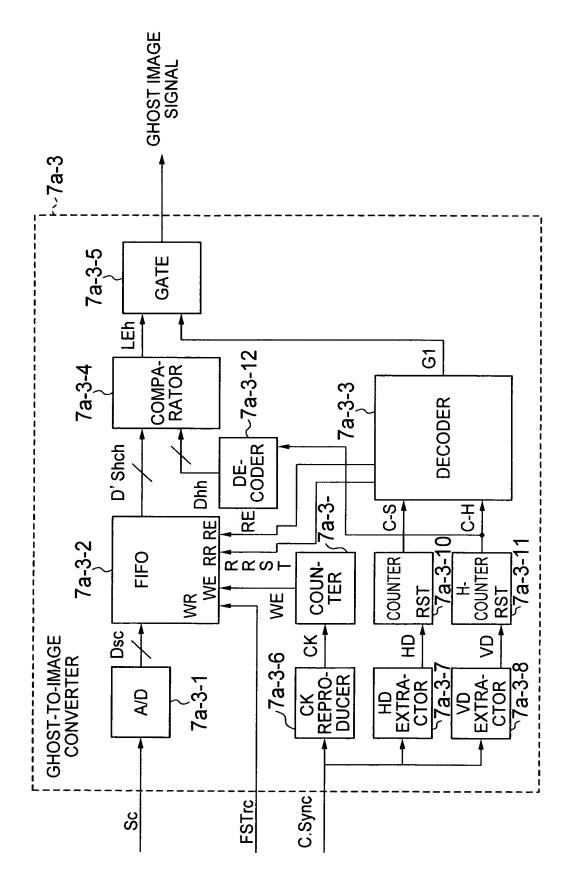


FIG. 28

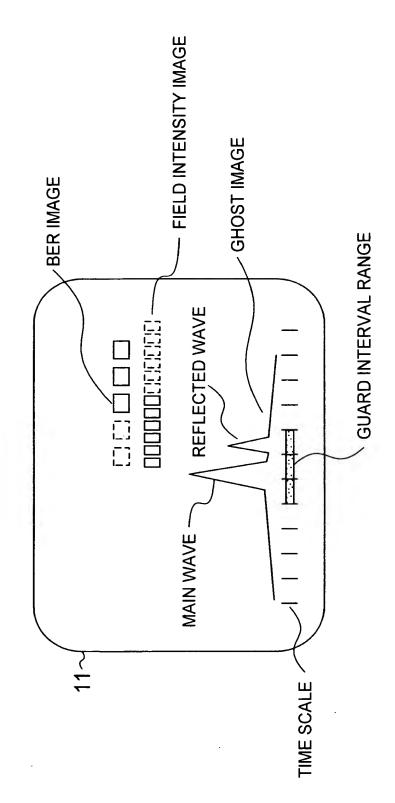


FIG. 29

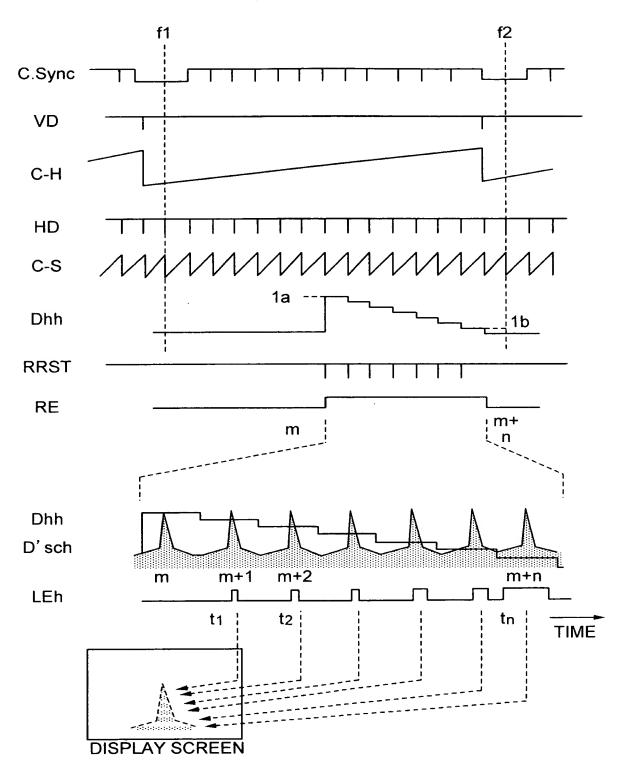


FIG. 30

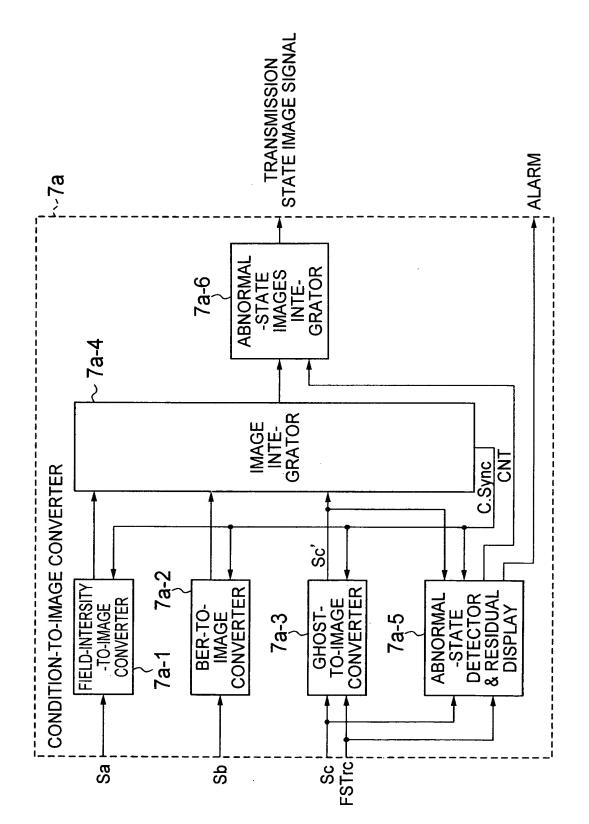


FIG. 31

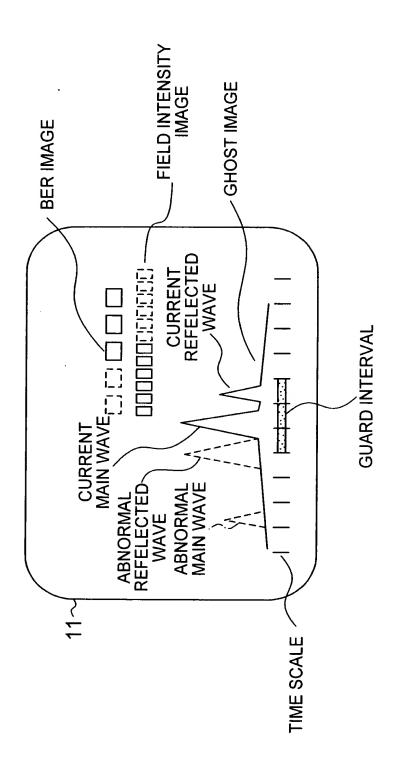
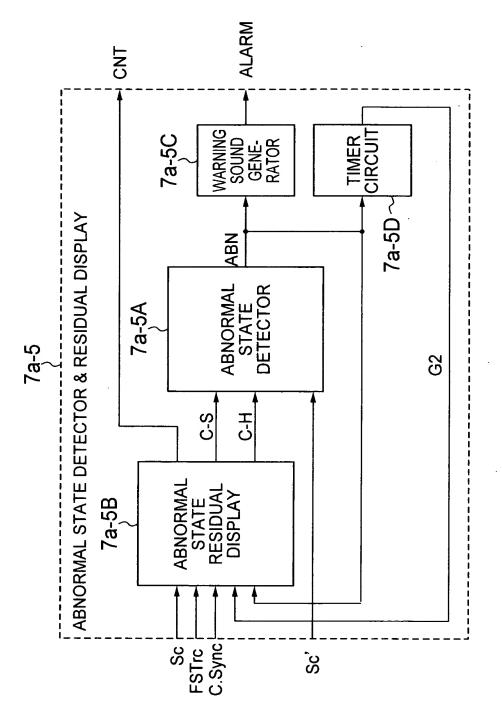


FIG. 32



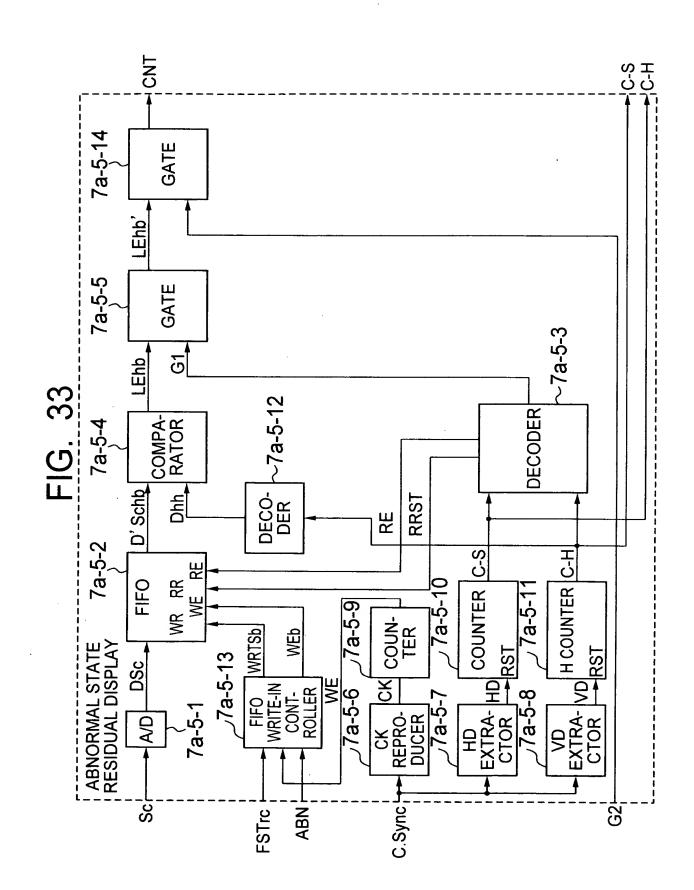


FIG. 34

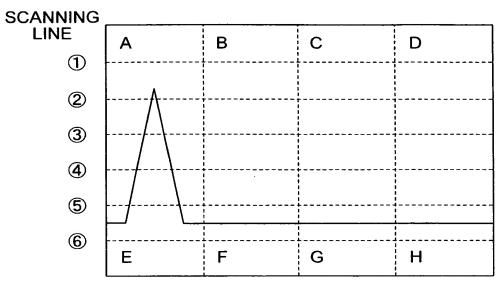


FIG. 35

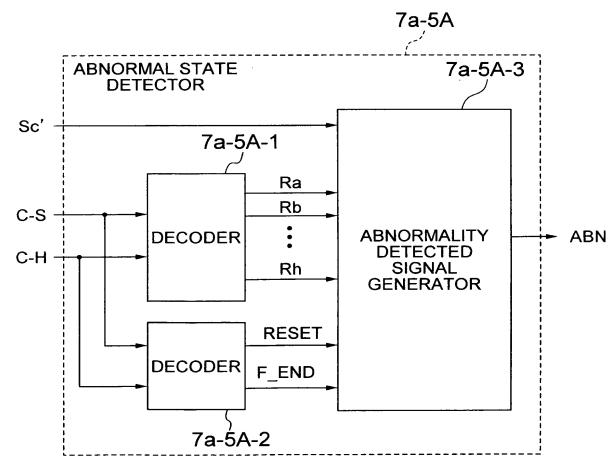


FIG. 36

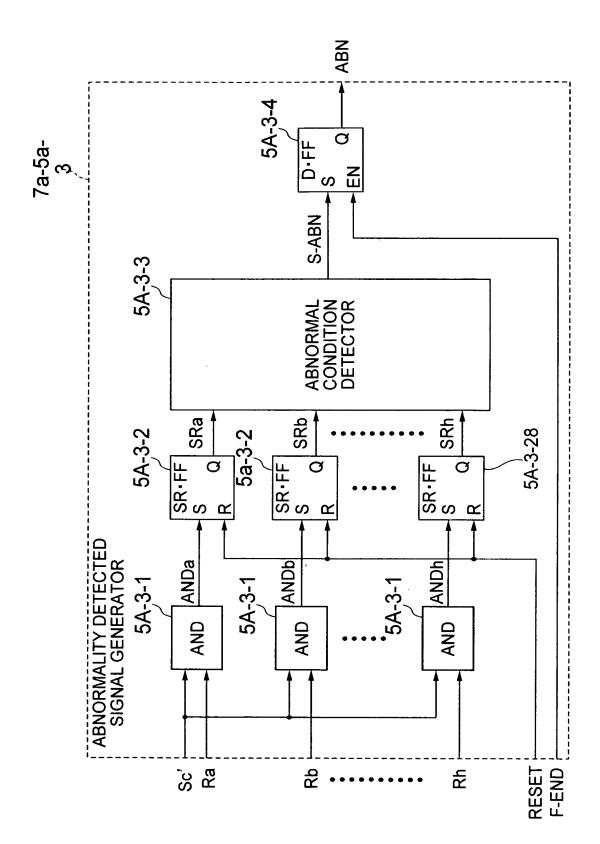


FIG. 37

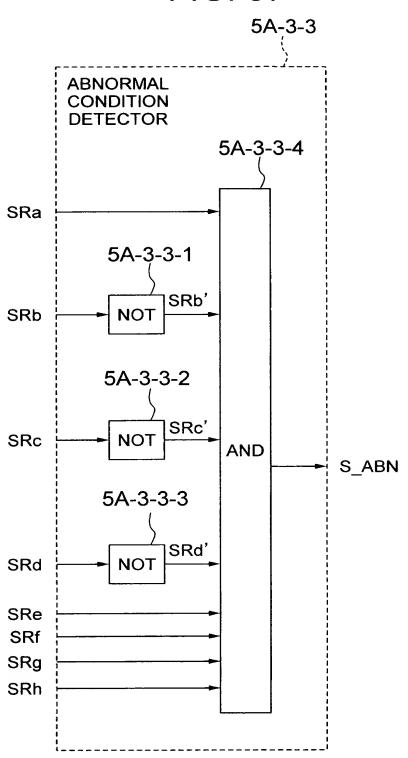


FIG. 38

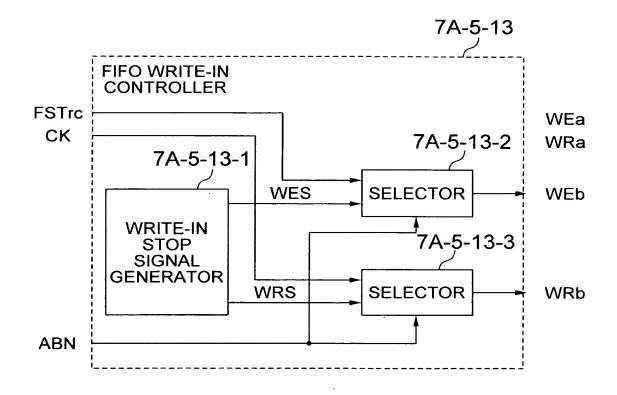


FIG. 39

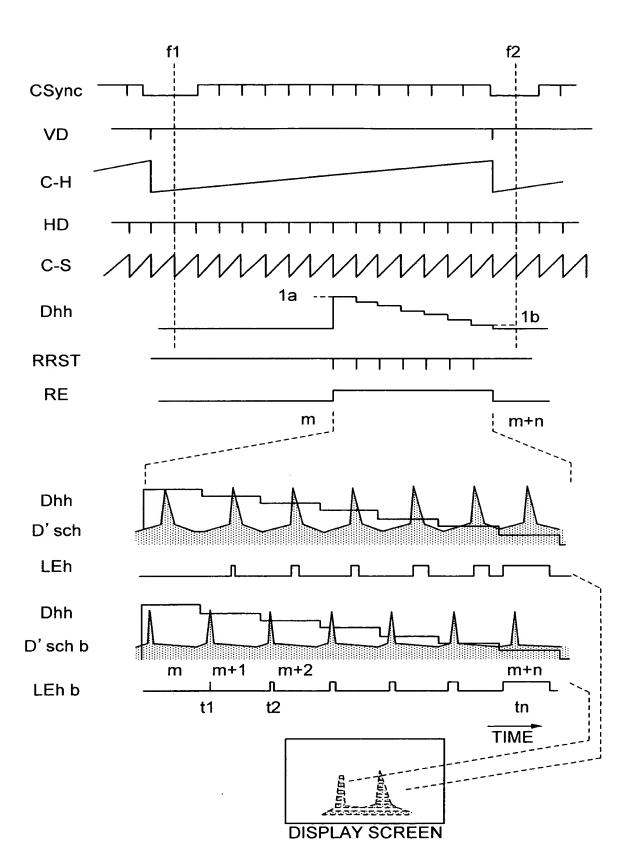


FIG. 40

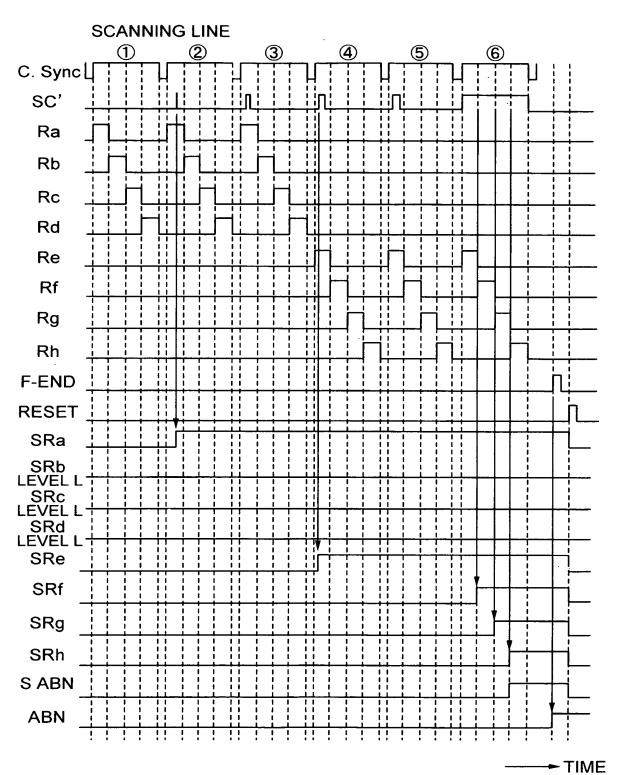


FIG. 41

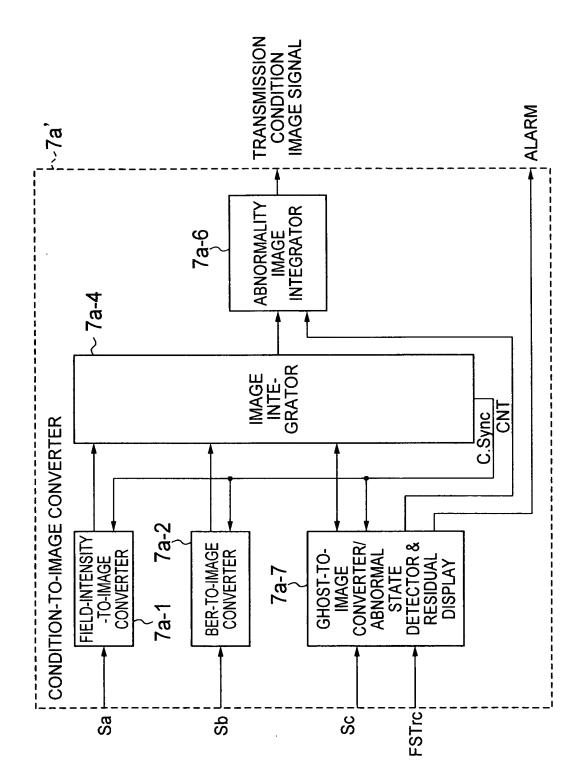


FIG. 42 7a-7 GHOST-TO-IMAGE CONVERTER/ABNORMALITY **DETECTOR & RESIDUAL DISPLAY** 7a-3-2 7a-3-1 7a-3-4 7a-3-5 **FIFO** D'Sch LEh Sc WR RR COMPA-**GATE** WE RE **RATOR** D' Schb 7a-5-4 7a-5-5 7a-5-14 **FIFO** LEhb <u>Ehb'</u> WR RR COMPA-7a-5-2 GATE GATE **-**¦CNT WE RE **RATOR** 7a-5-13 WRSTb 7a-3-5 **FIFO** Dhh FSTrc^L WRITE-IN CONT-WEb DECO-**ROLLER** G1 **DER** CK 7a-3-6 7a-3-9 RE CK COUN-RRST **REPRO-**C.Sync^l **TER** DUCER 7a-3-7 7a-3-10 7a-3-3 HD 7a-5D COUNTER C-H EXTRA- HD **CTOR** RST TIMER 7a-3-8 DECODER 7a-3-11 CIR-CUIT H-VD EXTRA- VD COUNTER **CTOR** RST 7a-5C **ABNORMAL** WAR-STATE NING **ABN GENERATOR** SOUND **GENE-**RATOR 7a-5A **ALARM**

FIG. 43

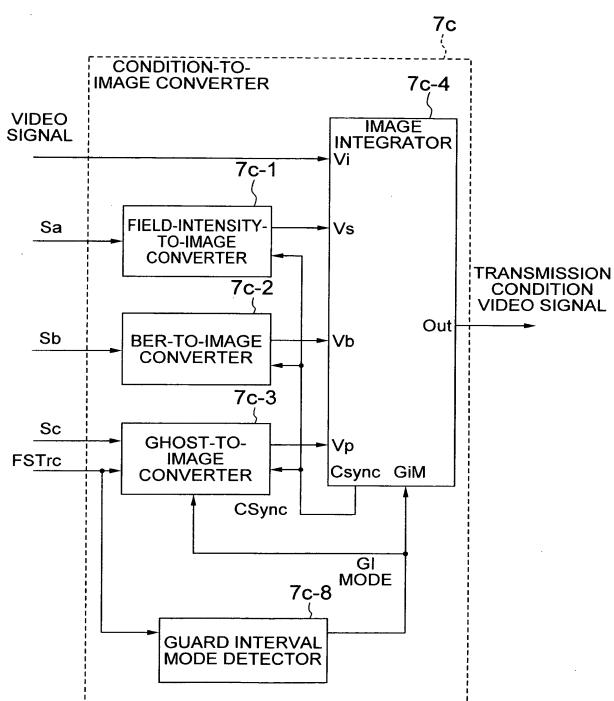


FIG. 44A

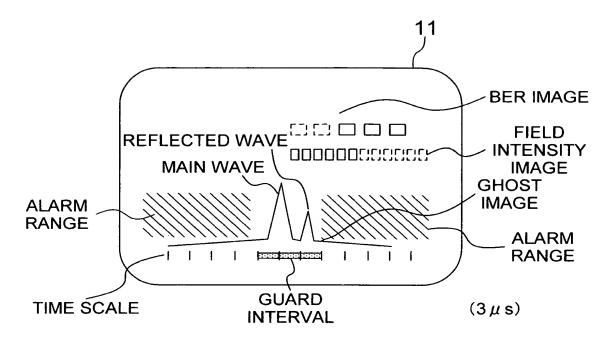


FIG. 44B

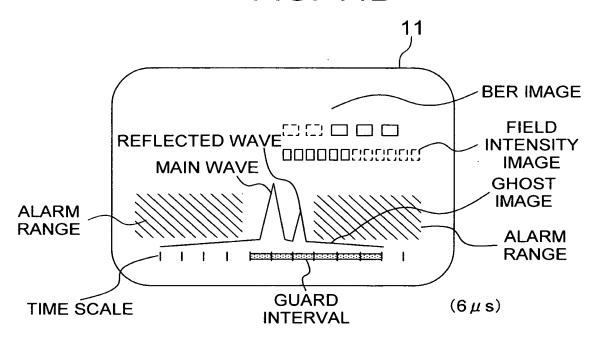


FIG. 45

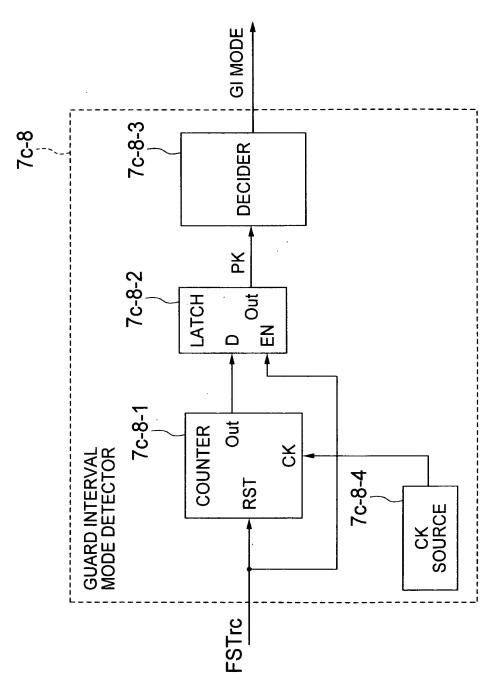


FIG. 46A

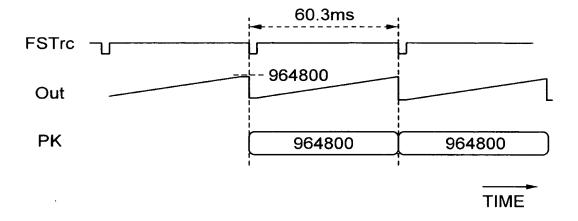
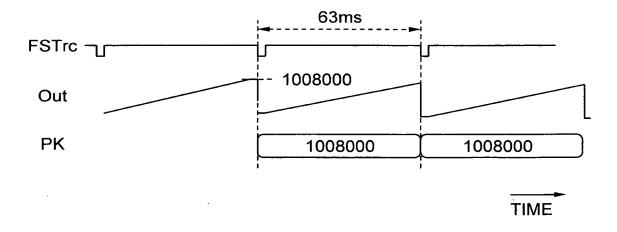


FIG. 46B



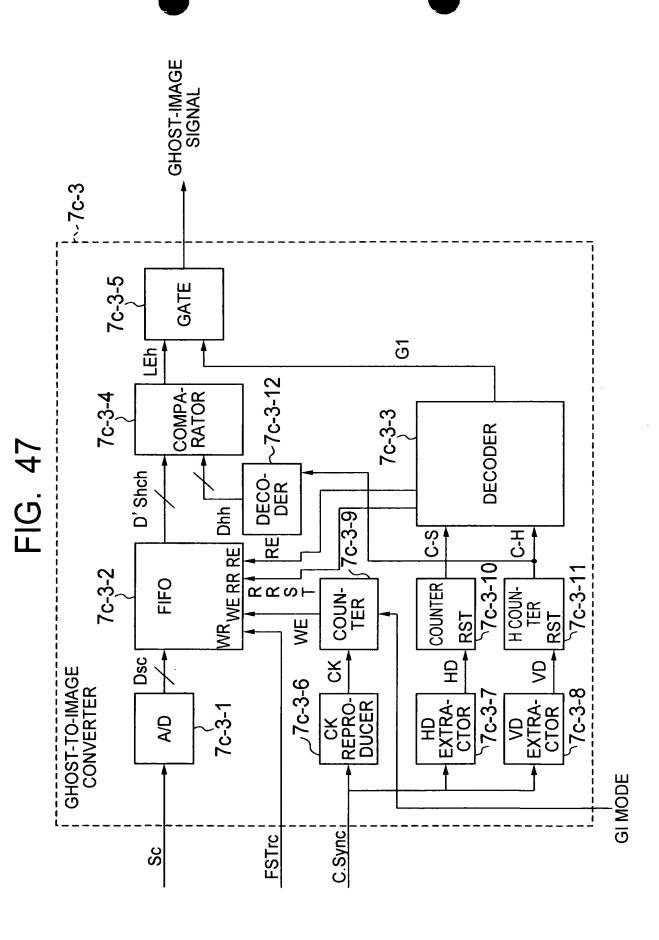


FIG. 48

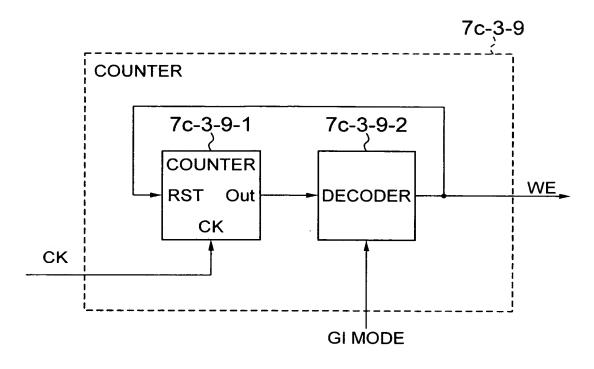


FIG. 49A

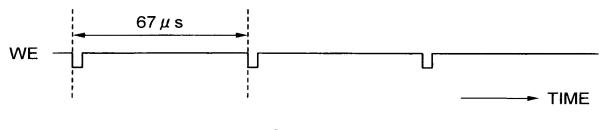
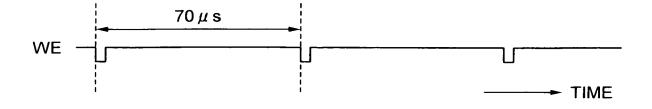
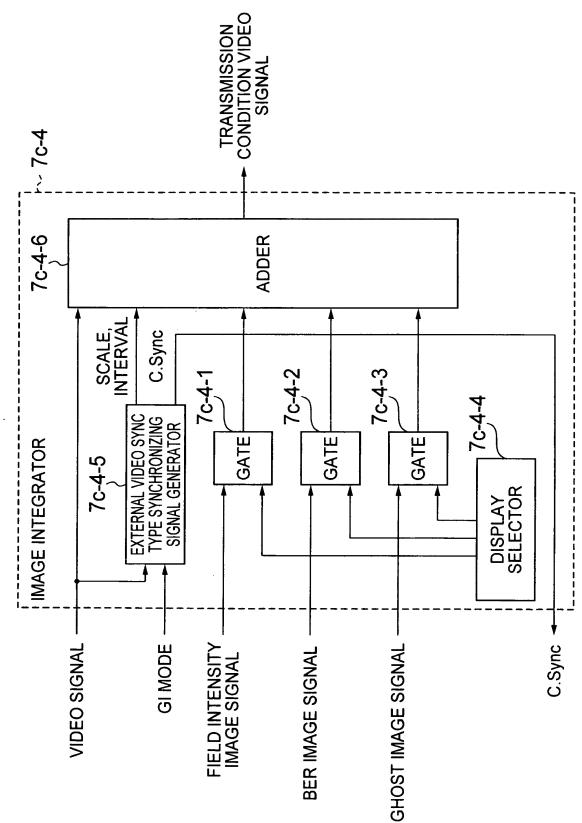
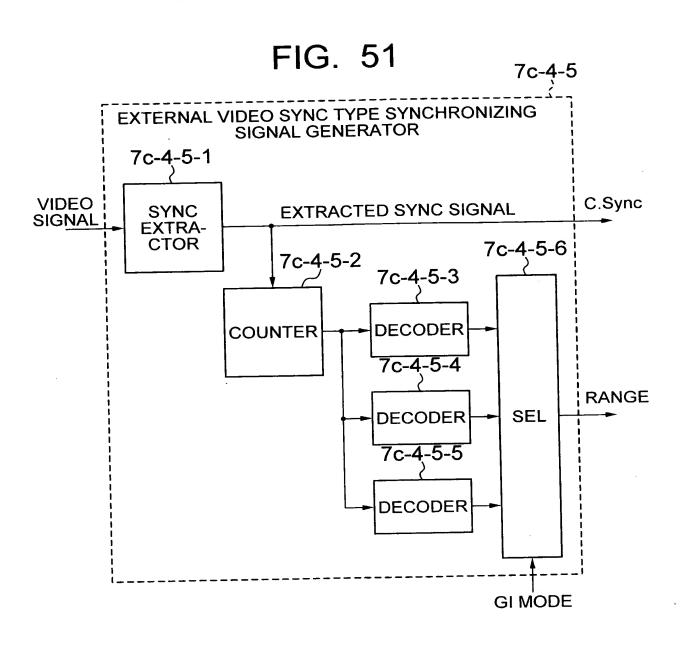


FIG. 49B









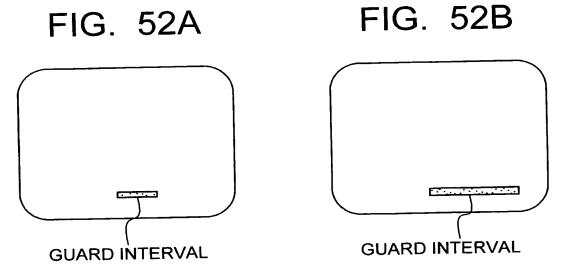


FIG. 53

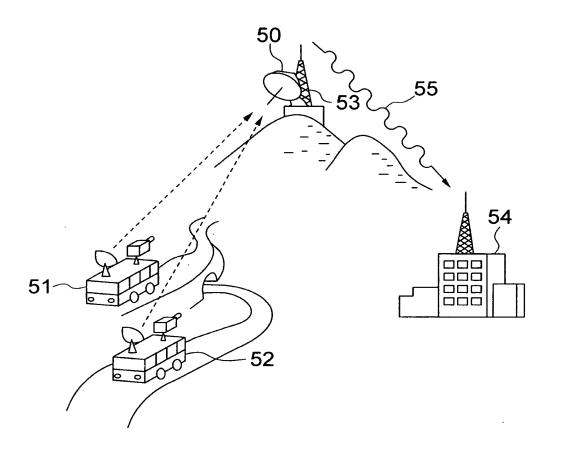


FIG. 54

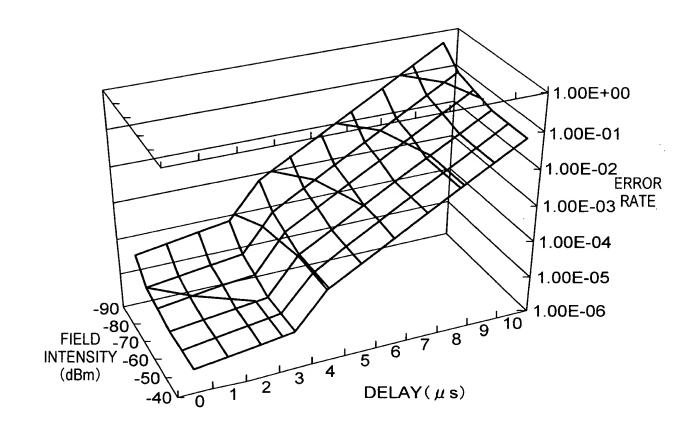
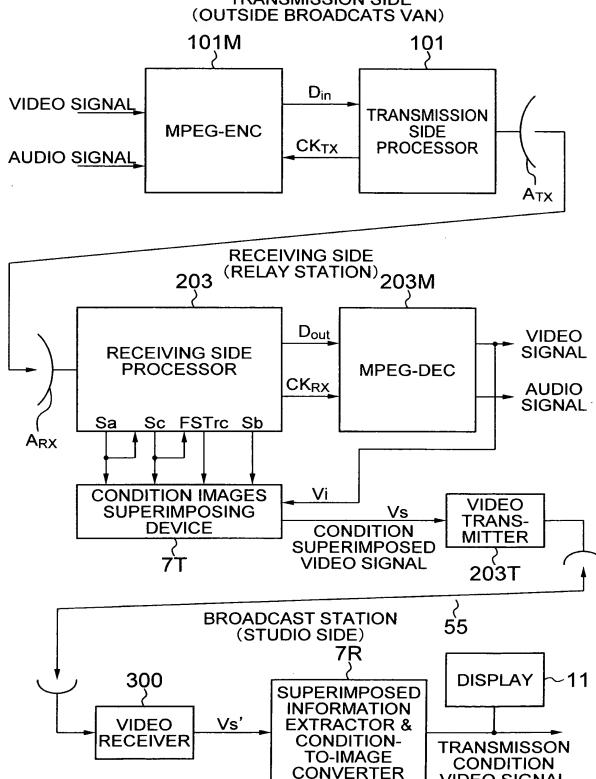


FIG. 55
TRANSMISSION SIDE



VIDEO SIGNAL

FIG. 56A

EXTERNAL INPUT VIDEO SIGNAL (203M OUTPUT)

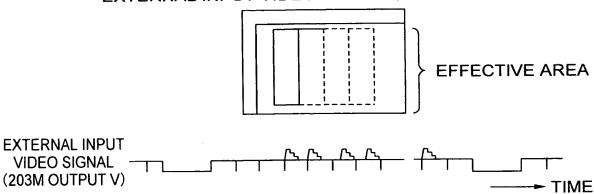


FIG. 56B

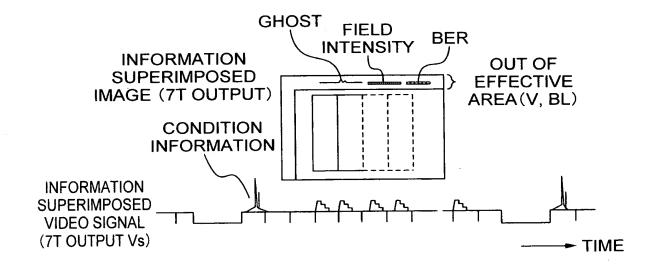


FIG. 56C

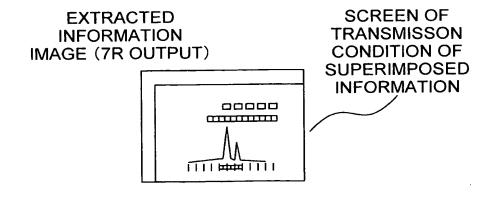


FIG. 57

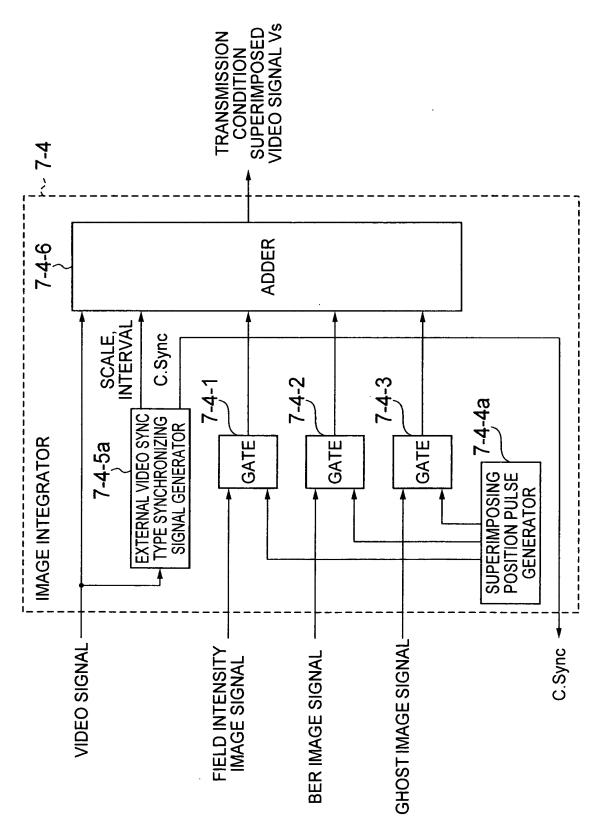


FIG. 58

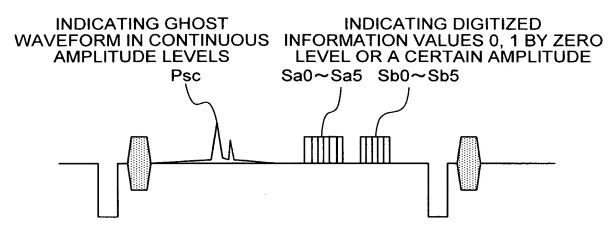


FIG. 59A

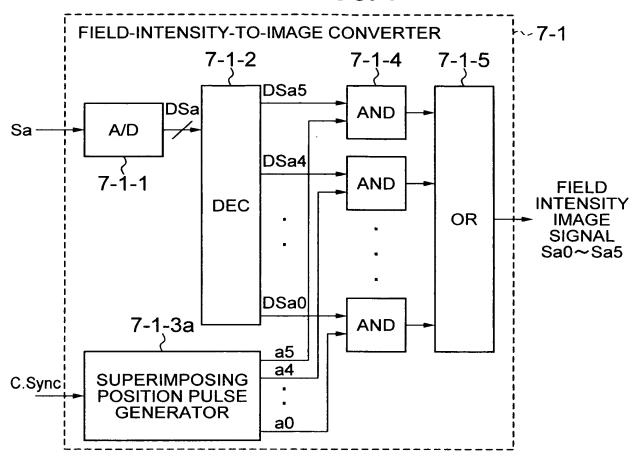


FIG. 59B

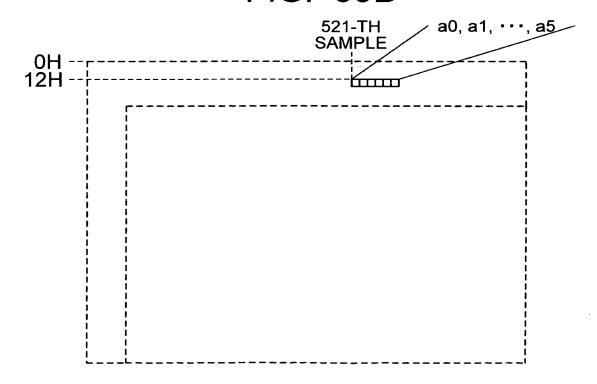


FIG. 60A

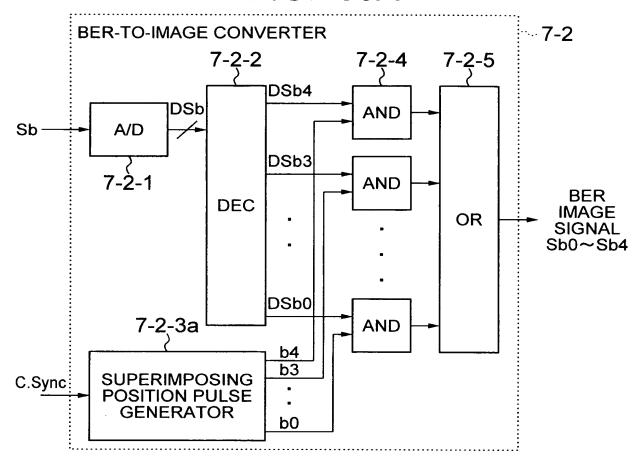


FIG. 60B

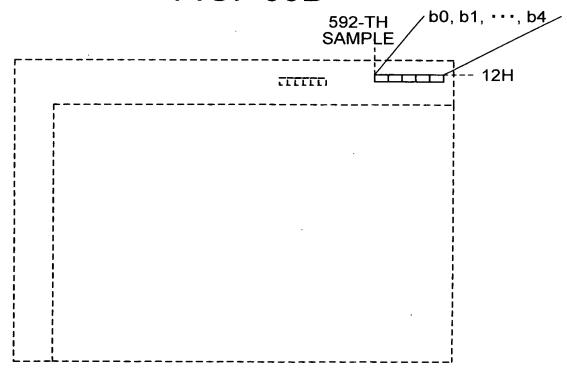
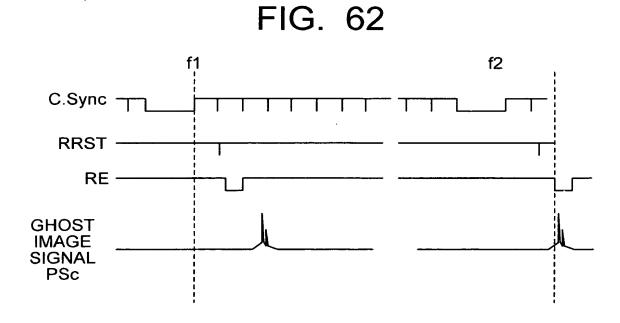


FIG. 61 7-3 **GHOST-TO-IMAGE CONVETER** 7-3-2 7-3-7 **GHOST** DSc D'Sc IMAGE SIGNAL Sc A/D D/A **FIFO** PSc 7-3-1 **FSTrc RRST** RE TIMING PULSE GENERATOR C.Sync



- TIME

FIG. 63A

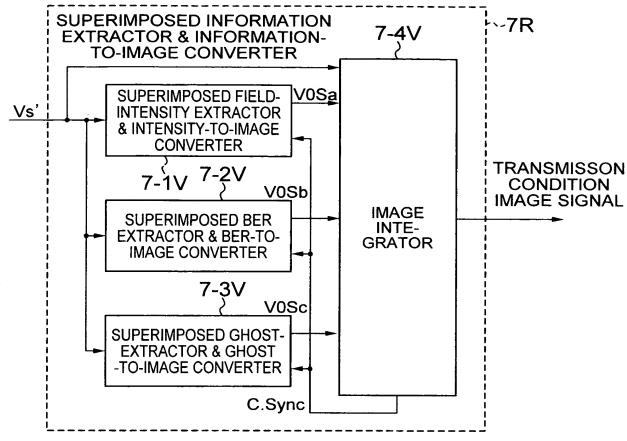
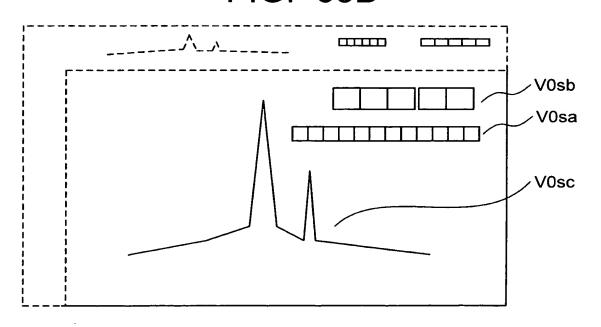


FIG. 63B



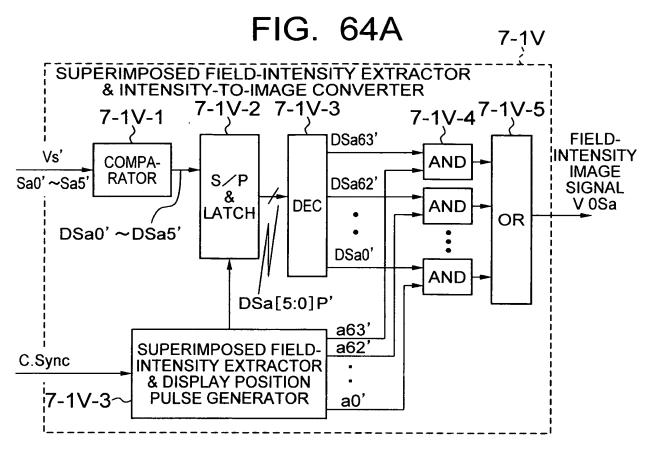
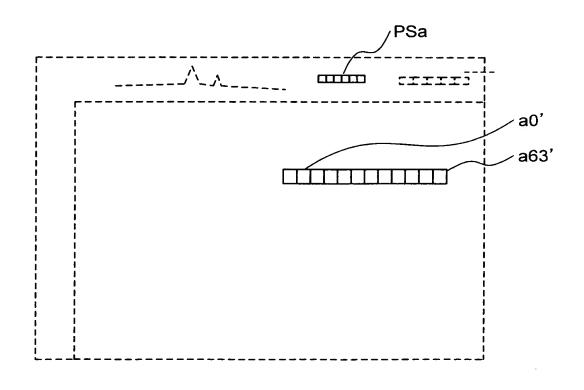


FIG. 64B



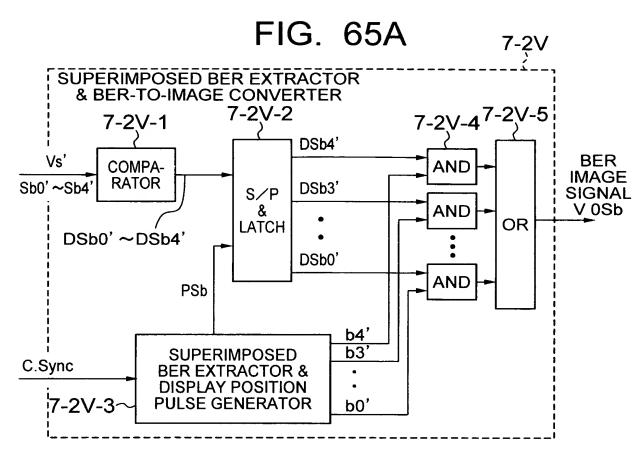


FIG. 65B

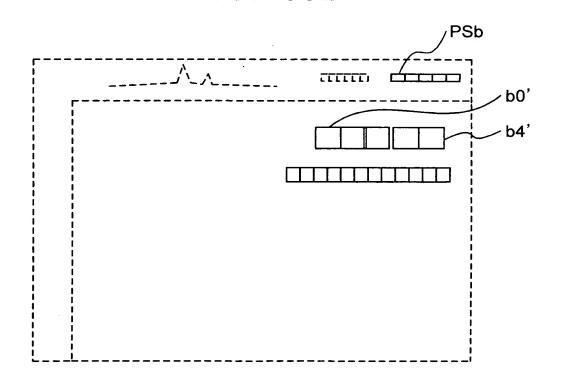


FIG. 66A

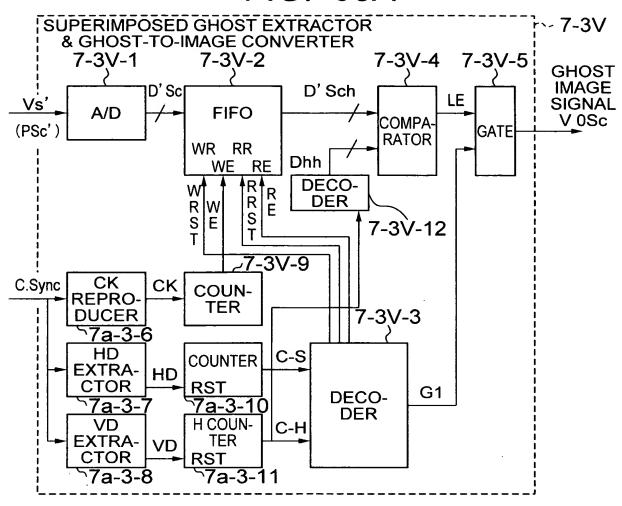
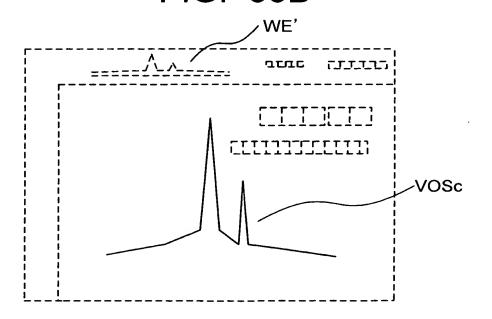
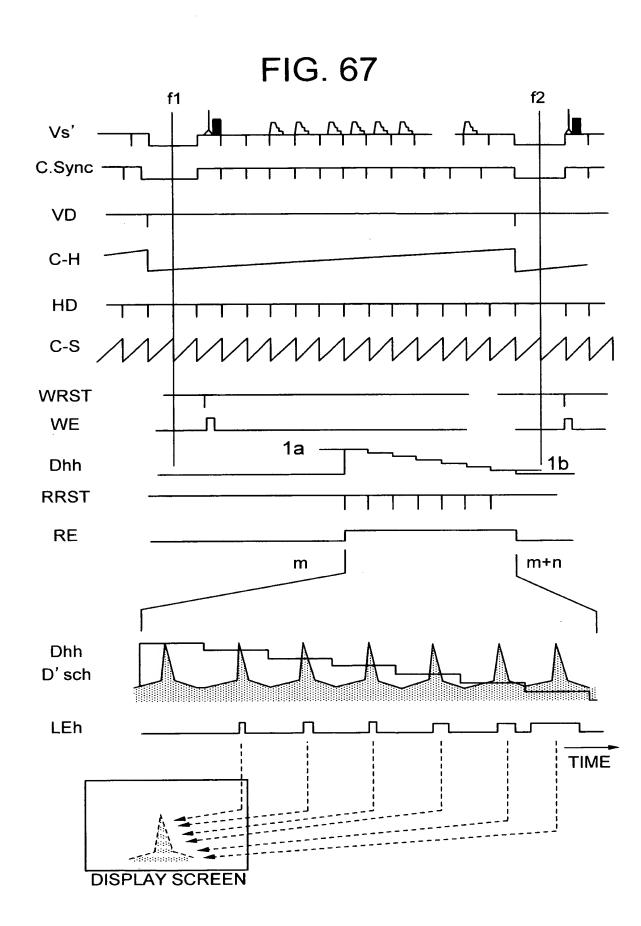


FIG. 66B





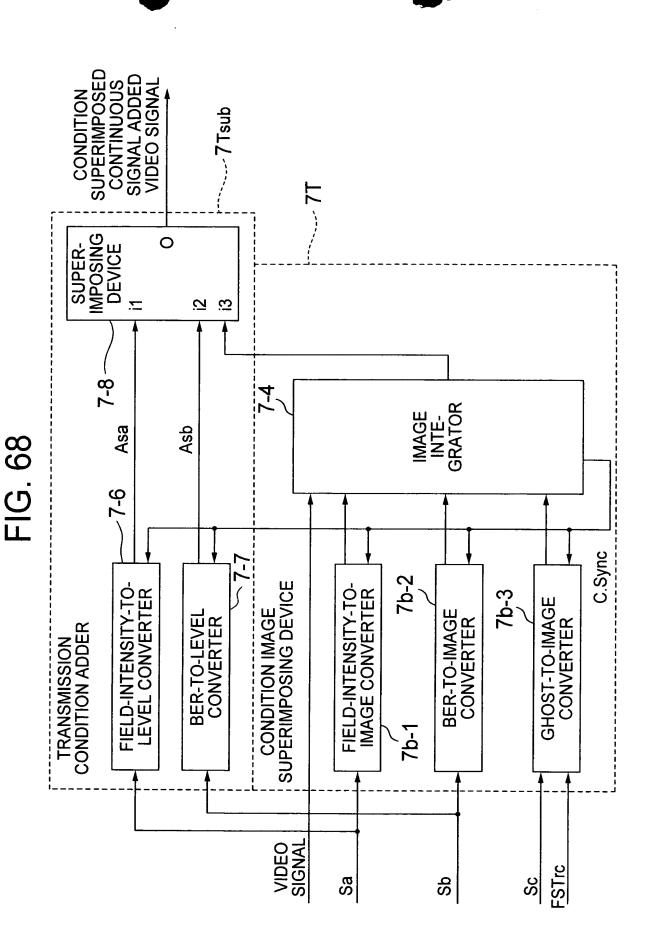


FIG. 69

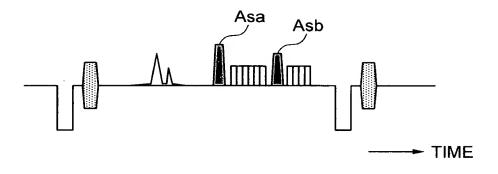


FIG. 70A

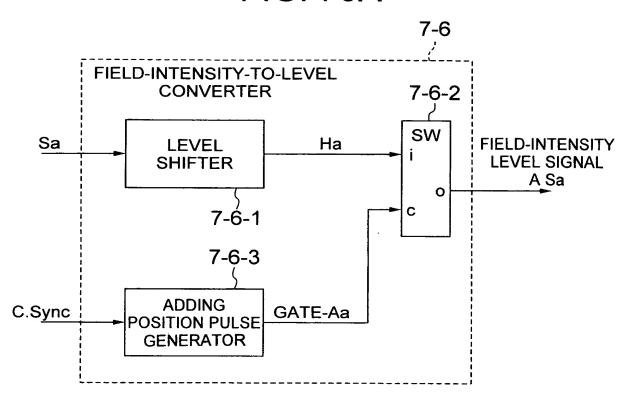


FIG. 70B

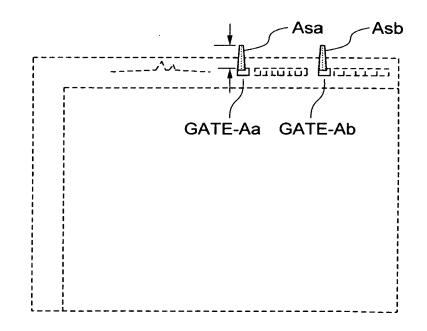


FIG. 71A

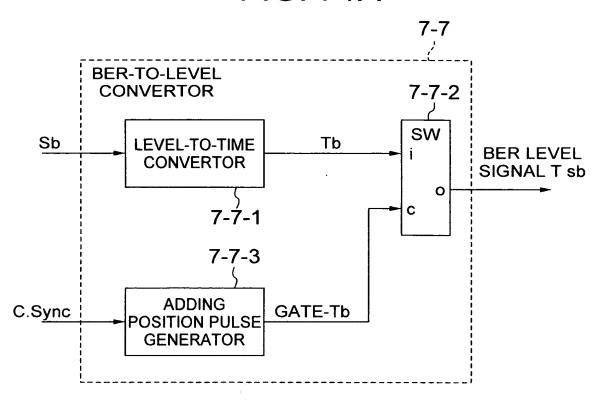


FIG. 71B

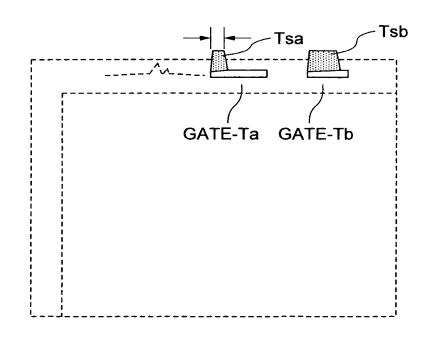


FIG. 72A

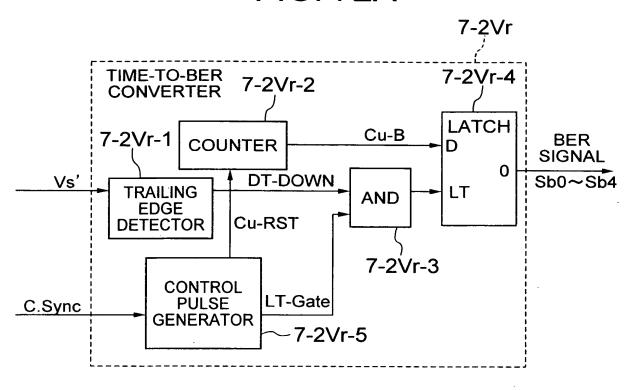


FIG. 72B

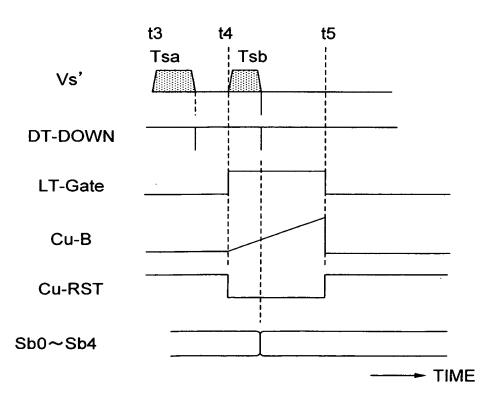


FIG. 73

